

ABSTRACT OF THE DISCLOSURE

A computing apparatus having a processor coupled to a memory. The memory has a data structure stored therein representing a set of summarized metrics of a plurality of data elements. The data structure includes: (a) an indication of an average value of the plurality of data elements; (b) a count value indicating a number of data elements in the plurality of data elements; (c) an indication of a minimum value and a maximum value of the plurality of data elements; and (d) an indication of a standard deviation of the plurality of data elements.